HALOGEN

FREE



# Vishay General Semiconductor

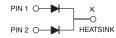
# **High Current Density Surface Mount Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.56 \text{ V}$  at  $I_F = 5 \text{ A}$ 

## TMBS® eSMP® Series



SlimDPAK (TO-252AE)



PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub> 20 A				
V <sub>RRM</sub>	120 V			
I <sub>FSM</sub>	150 A			
V <sub>F</sub> at I <sub>F</sub> = 10 A (T <sub>A</sub> = 125 °C)	0.66 V			
T <sub>J</sub> max.	150 °C			
Package	SlimDPAK (TO-252AE)			
Circuit configuration	Common cathode			

#### **FEATURES**

- Very low profile typical height of 1.3 mm
- Trench MOS Schottky technology
- Ideal for automated placement
- · Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
  - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in low voltage high frequency DC/DC converters, freewheeling diodes, and polarity protection applications.

#### **MECHANICAL DATA**

Case: SlimDPAK (TO-252AE)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and

AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER		SYMBOL	V20PW12C	UNIT
Device marking code			V20PW12C	
Maximum repetitive peak reverse voltage		$V_{RRM}$	120	V
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub> <sup>(1)</sup>	20	А
	per diode		10	Α
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	150	А
Operating junction temperature range		T <sub>J</sub> <sup>(2)</sup>	-40 to +150	°C
Storage temperature range		T <sub>STG</sub>	-55 to +150	°C

#### **Notes**

<sup>(1)</sup> With infinite heatsink

 $<sup>^{(2)}</sup>$  The heat generated must be less than the thermal conductivity from junction to ambient:  $dP_D/dT_J < 1/R_{\theta,JA}$ 



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	$I_F = 5.0 \text{ A}$	T <sub>A</sub> = 25 °C	V <sub>E</sub> (1)	0.64	-	V	
	I <sub>F</sub> = 10 A			0.84	0.92		
	$I_F = 5.0 \text{ A}$	T <sub>A</sub> = 125 °C	T = 105 °C	VF (··/	0.56	-	V
	I <sub>F</sub> = 10 A			0.66	0.74		
Reverse current per diode	$V_R = 90 \text{ V}$ $T_A = 25 \text{ °C}$ $T_A = 125 \text{ °C}$	]	0.01	-			
		T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	4	-	mA	
	I V <sub>R</sub> = 120 V ⊢	T <sub>A</sub> = 25 °C		-	0.7		
		T <sub>A</sub> = 125 °C		8	20		
Typical junction capacitance per diode	4.0 V, 1 MHz		CJ	800	i	pF	

#### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: pulse width ≤ 5 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V20PW12C	UNIT	
Tuning thermal registeres	R <sub>0</sub> JA (1)(2)	55	°C/W	
Typical thermal resistance	R <sub>0JM</sub> (3)	1.8	C/VV	

#### Notes

- $^{(1)}$  The heat generated must be less than thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$
- $^{(2)}$  Free air, mounted on recommended copper pad area; thermal resistance  $R_{\theta JA}$  junction to ambient
- $^{(3)}$  Mounted on infinite heat sink; thermal resistance  $R_{\theta JM}$  junction-to-mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V20PW12C-M3/I	0.20	I	4500	13" diameter plastic tape and reel	
V20PW12CHM3/I (1)	0.20	I	4500	13" diameter plastic tape and reel	

#### Note

(1) AEC-Q101 qualified

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## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

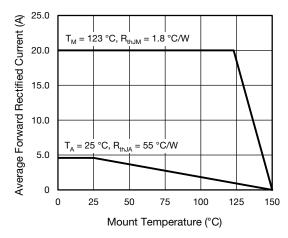


Fig. 1 - Maximum Forward Current Derating Curve

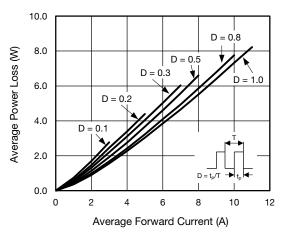


Fig. 2 - Forward Power Loss Characteristics

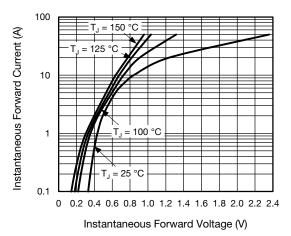


Fig. 3 - Typical Instantaneous Forward Characteristics

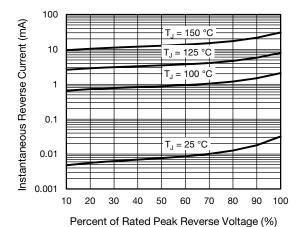


Fig. 4 - Typical Reverse Leakage Characteristics

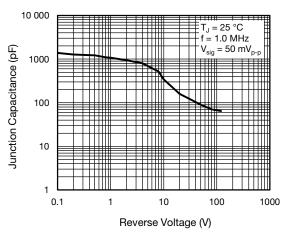


Fig. 5 - Typical Junction Capacitance

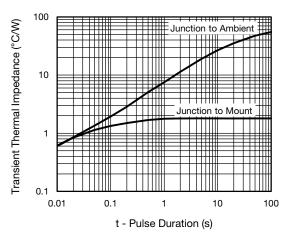


Fig. 6 - Typical Transient Thermal Impedance

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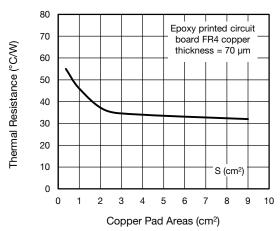
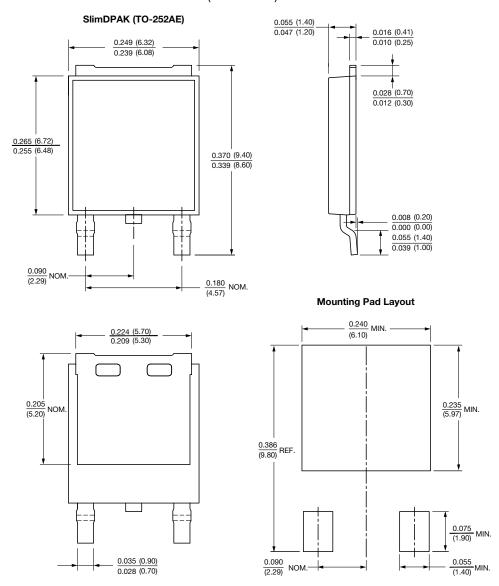


Fig. 7 - Typical Resistance Junction to Ambient vs. Copper Pad Areas

#### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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