<u>SPEC</u>	<u>IFICATION</u>		<u>SPEC. No.</u> DATE:		
То					
CUSTOMER'S PI	RODUCT NAME		JCT NAME ated capacitors di ×ムム2GA〇〇		
RECEIPT CO	ONFIRMATION				
		DATE		MONTU	
		DATE:	YEAR	MONTH	DAY
TDK Corporation Sales		DATE:	YEAR	MONTH	DAY
	E				DAY

# Handling precautions for High voltage ceramic capacitors Please read the following closely before using these products. Safety precautions The following precautions should be observed strictly to ensure safety design. Misuse of the product may lead to smoking of the product. Cautions 1.Operating voltage Use within the rated voltage of capacitor between terminals. For DC rated voltage application, you should control the peak voltage (Vo-p) under the rated voltage in case the AC voltage is superimposed on the DC voltage. Use within the rated voltage includes peak voltage (Vp-p) when AC voltage or impulse voltage applied in a circuit. Confirm irregular voltage (surge voltage, static electricity, switching noise, etc) occurs in the equipment used, and use within the rated voltage containing the irregular voltage. When the capacitor is used as a noise suppressor in the AC primary circuit, the voltage proof test should be within the specified conditions (voltage, time, wave form, etc). Connect by confirmation of non lose contact, and the voltage is started to apply to the circuit from zero to the specified voltage and it is stopped applying from the voltage to zero. (1) DC voltage Voltage (2) DC+AC voltage (3) AC voltage (4) Pulse voltage Voltage V<sub>P-P</sub> V<sub>0-P</sub> V<sub>0-P</sub> Measuring position 0-2. Operating temperature Be sure to use only those operating temperature described in our catalogue or specification. Keep the surface temperature under the maximum temperature, which includes the maximum self-heat temperature of 20 degree C. 3. Self-exothermal Self-exothermal temperature should be within 20 degree C on the condition of atmosphere temperature 25 degree C without the influence of wind such as the cooling fan. Be sure to use a capacitor in a circuit of current increase by AC voltage or pulse voltage applied. When high frequency voltage or impulse voltage applied in a circuit, reliability should be influenced. Take into considerations the load reduction and self-exothermal temperature, even if voltage should be within the rated voltage. PLP Spec No. HV095F19



SPEC No. :

$\land$	Cautions	
<ol> <li>Capacitance change of capacitors         For some of the capacitors, capacitance value             by applied DC voltage. And capacitor has agir             When you use the capacitor in the time consta             not.     </li> </ol>	ng characteristic (capacitance d	ecreases by keeping as it is
5. Vibration of capacitors When the capacitor class 2 is used in the AC of might occur in the specified frequency. Be su		-
<ul> <li>6. Usage of capacitance and storage</li> <li>Don't use capacitors in the following environm</li> <li>* Direct sunshine</li> <li>* Areas directly exposed to water or salty water</li> </ul>		
<ul> <li>* Areas that become dewy</li> <li>* Areas filled with toxic gases (such as hyd * Areas exposed to excess vibrations or sh Store capacitors in an environment from -10 to and use within the period after receiving the ca</li> </ul>	ock conditions described in our 0 40 degree C, with 15 to 70%F	catalogue or specification.
7. Inserting precautions When inserting capacitors into the PC board b (such as pressure of pusher, adjustment of clin chucking the body, or clinching the lead termin Distances between the hole position onto a PC When stretching the lead terminal, any force r damage to the insulation coating. Severe damage to the insulation coating.	nching portion) and minimize th nals. C board should be equal to the nay load the bottom of the capa	e impact force by pitch of capacitors. acitor body and result in
8. Soldering Don't immerse the capacitor body into the more soldering. Use PC board, and solder the term such as pre-heat temperature, soldering temp descriptions in our catalogue or specification. Adjust the amount of solder within the proper When using soldering iron for installing capace and temperature control should be used. We 3.5±0.5s. as 1 time, and you should use an a well as a proper wattage (50W Max.). Do except for the terminals of capacitor.	inals in the opposite side of the perature, and soldering time, sh (refer to Fig1) volume. Select an appropriate itors or reworking onto the PC recommend that the iron cond dequate tip diameter (φ3mm M	body. Soldering conditions nould be followed by the soldering material. board, sufficient pre-heating ition is 350±10 degree C/ lax.) with the soldering iron
9. Flux When using flux for soldering capacitors onto the Flux will be composed of halogenated mater Don't use a strong acid grade of flux. When be done.	ial less than 0.1 wt% (cl conv	ersion).



SPEC No. :

# ✓ Cautions 10. Cleansing When the cleansing should not be sufficient, the cleansing liquid or any residue might leave on the capacitor body, they may deteriorate the insulation coating or performance (insulation resistance, etc.). When using ultrasonic cleansing, avoid transmitting vibrations onto the PC board. Conditions of ultrasonic cleansing, such as output frequency and time of the method, should be taken into considerations. After cleansing capacitors, dry them well. Cleansing liquid should not contain electrolyte, nor leave any residue. Through the result of the cleansing method, confirm whether the quality of the capacitors have been affected due to the conditions. 11. Coating or molding When coating or molding capacitors after installing components onto the PC board, confirm whether the performance of capacitors may not be damaged by the work. 12. Mechanical stress Don't submit to excessive mechanical shock. Don't use capacitors which may have been damaged due to dropping, etc. If possible, avoid bending the terminals of capacitors. In an unavoidable case of bending, use a small jig to decrease the mechanical stress on the capacitors. 13. Others Please contact TDK before using our capacitors listed in this catalogue or specifications for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property, or when intending to use one of our capacitors for other applications than specified in this catalog or specifications. \* Medical equipment \* Aerospace equipment \* Power plant equipment \* Aircraft equipment \* Transportation equipment (vehicles, trains, ships, etc) \* Undersea equipment \* Traffic signal equipment \* Disaster prevention, crime prevention equipment \* Data processing equipment exerting influence on public \* Application of similar complexity and, or reliability requirements to the applications listed in the above. Please refer to the guideline of notabilia for fixed ceramic capacitors issued by JEITA (Japan Electronics and Information Technology Association, EIAJ RCR-2335). PLP Spec No. HV095F19



SPEC No. :

electromagnetic interference supp	nic insulated capacitors disc type used pression in electronic and equipment ar 0384-14 and relative safety standards.	
Relative standards IEC 60384-14、EN60384-14、U	L60384-14	
<ul> <li>Mention item</li> <li>1. Applicable safety standard app</li> <li>2. Acquired safety standard appr</li> <li>3. Part Name</li> <li>4. Operating temperature range</li> <li>5. Test condition</li> <li>6. Performance</li> <li>7. Marking</li> <li>8. Figure &amp; Dimension</li> <li>9. Label &amp; Transport</li> <li>10. Notification before the modification</li> </ul>	oval	
We do not use the following materia (1) PBBs (Poly Bromo Bipheny	al (1),(2) in these products. PBBOs (Po /ls)	bly Bromo Biphenyl Oxides)
We do not use Class I and II ODS	(Ozone depleting substances) in all our pro	cess of these products.
These products shall conform to Ro	oHS Directive.	
These products are Halogen-free.(I	Br≦900ppm, Cl≦900ppm, Br+Cl≦150	00ppm)
Manufacturing place Manufacturing site should be TDł	< Taiwan & TDK Xiamen	
Division	Date Issued	SPEC No.
Ceramic Capacitors Business Group		



1. Applicable safety standard

This is specification applies the BSI, VDE, SEV, SEMKO, NEMKO, DEMKO, FIMKO, IMQ, SAA, UL, CSA, CQC and KTL approved ceramic capacitor disc type.

- Approval report No. Safety W.V Standard No. of IEC Standard No. T.C. Subclass Standard Taiwan Xiamen BS EN 60065 BS EN 60384-14 (8.8, 14.2) BSI KM37103 IEC 60384-14 BS EN 60384-14 VDE 40017931 SEV 15.0121 SEMKO 1406954 NEMKO EN 60384-14 P12215264 X1:440VAC DEMKO D-01094 X1, Y1 Y1:400VAC FIMKO SL, FI 27387 Β, IMQ V3691 Z5U IEC 60384-14 SAA AS3250 CS6268 CSA CSA-E60384-14 1785504 UL UL60384-14 E37861 CQC1200 CQC1000 CQC GB/T14472 1082617 1052863 SU03047 SZ03001 440V AC X1 -12002 -12002 KTL K60384-14 SZ03001 SU03047 Y1 400V AC -12004 -12004
- $2\,.\,\,$  Acquired safety standard approval and Approval report No.

\* T.C.: Temperature Characteristic.

\* Certificate No(s) shall be changed owing to the revisions of the related standards and renewal of certificate.



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# 3. Part name

#### (Example)

	inpic	,									
<u>CD</u>	<u>90</u>	<u>ZU</u>	<u>2GA</u>	<u>222</u>	<u>M</u>	<u>Y</u>	<u>G</u>	<u>K</u>	A	<u> </u>	
									l		Halogen-free
											Safety (Miniature Type)
											Lead style XNote-2
											Internal code
											Rated capacitance tolerance
											Rated capacitance
											Rated voltage
											Temperature characteristic of capacitance %Note-1
											Shape
											Туре

#### XNote-1

Temperature characteristic of capacitance : SL:TC SL, -B:TC B, ZU:TC Z5U

#### X Note-2

Lead style G : Vertical kink long lead (Bulk) N : Vertical kink short lead (Bulk)

4. Operating Temperature range : -25 °C to +125 °C

Operating temperature range max. is +125 °C

(Including capacitor's self-heating max. +20 °C)

5. Test condition

Test and measurement shall be made at the standard condition, (Temperature 15 to 35  $^{\circ}$ C, relative humidity 45 to 75 % and atmospheric pressure 860 to 1060 Pa.),Unless otherwise specification herein. If doubt occurred on the value of measurement, and remeasurement was requested by customer capacitors shall be measured at the reference condition (Temperature 20 ±2  $^{\circ}$ C, relative humidity 60 to 70 % and atmospheric pressure 860 to 1060 Pa.)

6. Performance

The performances shall comply with Table-1



No.		ems	Performance	Test method		
1	Appearance dimension	e and	The appearance and dimension shall be as given in paragraph 8 and Table-2 to 3	Visual check and measuring with calipers.		
2	Marking		The marking shall be easily legible (Paragraph 7)	Visual check		
3	Withstand Between voltage terminals		No failure	Voltage: 4000V AC (50 or 60 Hz) Test time: 60 s Charge and discharge current shall be 50 mA or less.		
		Between terminal and exterior cladding	No failure	4000VAC (50 or 60Hz) shall be applied for 60 s between the terminal connected together and the enclosure of capacitor with metal foil from the distance 2.6mm of the body.		
4	Insulation resistance	Between terminals	10000 M $\Omega$ or more	60±5 sec. after application with 500±50V DC.		
5	Capacitanc	e	With the tolerances specified with Table-2 to 3	SL: Measuring frequency : 1MHz ±10 % Measuring voltage : 5Vrms. or less		
6	Dissipation ( tanō )	factor	SL : 0.5 % or less B,Z5U : 2.5 % or less	B,Z5U: Measuring frequency : 1kHz ±20 % Measuring voltage : 5Vrms. or less		
7	<ul> <li>7 Capacitance temperature characteristic (No voltage application)</li> </ul>		SL : Within -1000 to +350ppm/°C B : Within ±10 % Z5U : Within – 56% to + 22%	Standard temperature: 20°C (Z5U: 25°C) Temperature range: SL: +20 to +85°C B: -25 to +85°C Z5U: +10 to +85°C Initial :pre-heat 125±2°C, 1h, Leaving room temp. for24±2h.		
8	Strength of terminal	Tensile strength	Lead wire shall not be disconnected, and capacitor shall not be damaged.	The force of 10N shall be applied to the axial direction of the termination.		
	Bending strength		Lead wire shall not be disconnected, and capacitor shall not be damaged.	The force of 5N shall be applied to the axial direction of the terminal and the body shall be inclined through an angle of 90 degrees, then the body shall be returned to the original position. Furthermore the body shall be inclined to the other direction of 90 degrees. This operation shall be carried out two times.		
9	resistance Vibration	Appearance Capacitance	No marked defect Within the tolerances	Vibration frequency range: 10 to 55Hz. Displacement: 0.75mm		
	VIDIALION	change	specified with No.5	Total duration: 6 hours		
		Dissipation factor ( tan δ )	Within the value specified with No.6	(2 hours for each direction: X,Y,Z)		

	Table-1 Continue								
No.	Iter	ns	Performance	Test method					
10	Resistance to	Appearance	No marked defect	Soldering temperature:350±10°C/3.5±0.5					
	soldering heat	Capacitance change	Within ± 10 %	or 260±5°C/10±1 s Dipping depth: 1.5 to 2.0mm from the bottom of lead terminal.					
		Withstand voltage (Between terminals)	No failure	(shielding board shall be used.) Initial :pre-heat 125± 2°C, 1h. Leaving room temp. for 24± 2h. After test: leaving room temp. for 24± 2h.					
11	Solder ability		At least 3/4 of circumferential dipped into solder shall be covered with new solder.	Soldering temperature : $245 \pm 5$ °C Dipping time : $2 \pm 0.5$ sec. Concentration of solution shall be about 25 % colophonium in weight ratio.					
12	Temperature	Appearance	No marked defect <sub>o</sub>	Temperature cycles first, then dipping cycle					
	cycle and dipping cycle	Capacitance change	SL,B: Within ± 10% Z5U : Within ± 20%	should be tested. Temperature cycle: 5 cycles Step 1: - 25°C, 30 min.					
		Dissipation factor ( tan δ )	SL : 1.0% or less B, Z5U : 5.0% or less	Step 2: room temp., 3 min. Step 3: + 125°C, 30 min.					
		Insulation resistance	1000 M $\Omega$ or more	Step 4: room temp., 3 min. Dipping cycle: 2 cycle Step 1: + 65°C, 15 min.					
		Withstand voltage Between terminals	No failure	Step 1: 105 C, 15 min. Step 2: 0°C, 15 min. (saturated aqueous solution of salt) Initial :pre-heat 125±2°C, 1h. Leaving room temp. for 24± 2h. After test: leaving room temp. for 24± 2h					
13	Moisture	Appearance	No marked defect	Test temperature : 40 ±2 °C					
	resistance	Capacitance	Within ±15 %	Relative humidity : 90 to 95 %					
	(Steady state)	change Insulation	3000 MΩ or more	Test time : 500 +12, -0 hours Capacitors shall be measured after leaving					
		resistance		it under room temperature for 1 to 2 hours.					
		Withstand	No failure						
14	Moisture	Appearance	No marked defect	Test temperature : 40± 2 °C					
	resistance loading	Capacitance	Within ± 15%	Relative humidity: 90 to 95% Test time : 500+ 12,-0 hours					
	localing	change		440V AC applied.					
		Insulation resistance	3000 M $\Omega$ or more	Capacitors shall be measured after leaving					
		Withstand voltage	No failure	it under room temperature for 1 to 2 hours. Charging and discharging current shall be 50mA or less.					
15	High tempera-	Appearance	No marked defect	1)Impulse voltage test					
	ture loading	Capacitance change	Within ± 20%	1.2/50µs 8kVpeak/3times. 2)High temperature loading					
		Insulation resistance	3000 MΩ or more	Test temperature : 125± 3 °C Test time: 1,000+24,-0 hours					
		Withstand Voltage	No failure	680VAC applied. (The voltage is increased to 1000Vrms for 0.1sec. once every hour) Initial :pre-heat 125±2°C, 1h. Leaving room temp. for24±2h. After test: leaving room temp. for 24±2h Charge and discharge current shall be 50mA or less.					



7. Marking

Marking on one side.

(1) Type : CD

(2) Rated capacitance tolerance

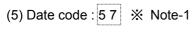
Example 2200 pF : 222 ± 20% : M

(3) Subclass

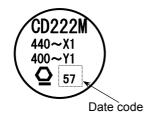
Sub class	Rated voltage	Marking		
X1	440 V AC	440~X1		
Y1	400 V AC	400∼Y1		

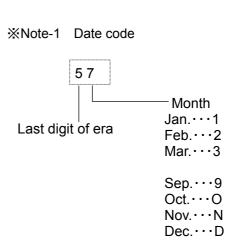
(4) Manufacture's trade mark

Ŏ	Taiwan
Q	Xiamen



Example



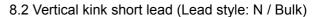


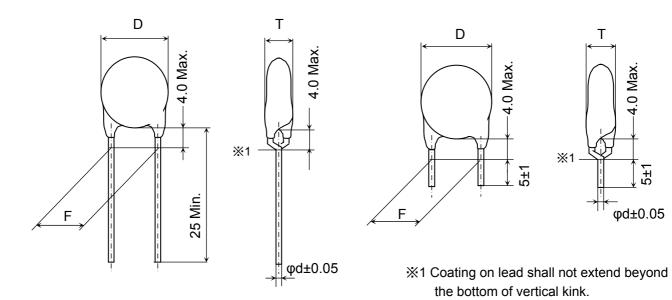


-6-

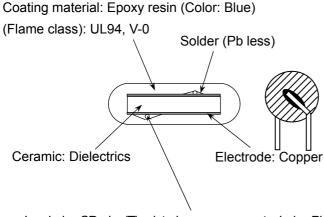
#### 8. Figure & dimension

8.1 Vertical kink long lead (Lead style: G /Bulk)



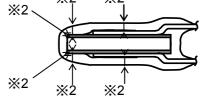


Unit: mm



Lead wire: CP wire (Tin plated copper covers steel wire, Pb less)

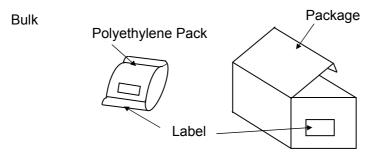
2 Coating thickness is 0.4mm Min.
from the live part.
2 2 2 2



#### 9. Labe1 and transport

Capacitors shall be packaged prior to shipment so as to prevent damage during transportation and storage. Shipping carton contains the following information on the label.

- a) TDK item name
- b) Quantity
- c) TDK inspection number
- d) Manufacturer's name
- e) Country of origin



10. Notification before the modification

We'll previously notify the modified place of manufacture, manufactured articles and materials.



# <u>Type : CD</u> T.C : SL, B, Z5U

## Vertical kink long lead (lead style: G / bulk)

			Table-2					
Your part No.	TDK part No.	T.C.	Cap.	C-Tol.	Dime	nsion	(Unit : mm)	
Tour part No.	TDR part No.	1.0.	(pF)	(%)	D Max.	Т Мах.	F	φd
	CD45SL2GA100JYGKA	SL	10	± 5	7.0	7.0	10+2,-1	0.6
	CD45SL2GA150JYGKA	SL	15	± 5	7.0	7.0	10+2,-1	0.6
	CD45SL2GA220JYGKA	SL	22	± 5	7.0	7.0	10+2,-1	0.6
	CD45SL2GA330JYGKA	SL	33	± 5	7.0	7.0	10+2,-1	0.6
	CD45SL2GA470JYGKA	SL	47	± 5	8.0	7.0	10+2,-1	0.6
	CD45SL2GA680JYGKA	SL	68	± 5	9.0	7.0	10+2,-1	0.6
	CD70-B2GA101KYGKA	В	100	± 10	7.0	7.0	10+2,-1	0.6
	CD70-B2GA151KYGKA	В	150	± 10	7.0	7.0	10+2,-1	0.6
	CD70-B2GA221KYGKA	В	220	± 10	7.0	7.0	10+2,-1	0.6
	CD75-B2GA331KYGKA	В	330	± 10	7.5	7.0	10+2,-1	0.6
	CD85-B2GA471KYGKA	В	470	± 10	9.0	7.0	10+2,-1	0.6
	CD65ZU2GA681MYGKA	Z5U	680	± 20	7.0	7.0	10+2,-1	0.6
	CD70ZU2GA102MYGKA	Z5U	1000	± 20	7.0	7.0	10+2,-1	0.6
	CD80ZU2GA152MYGKA	Z5U	1500	± 20	8.0	7.0	10+2,-1	0.6
	CD90ZU2GA222MYGKA	Z5U	2200	± 20	9.5	7.0	10+2,-1	0.6
	CD11ZU2GA332MYGKA	Z5U	3300	± 20	12.0	7.0	10+2,-1	0.6
	CD12ZU2GA472MYGKA	Z5U	4700	± 20	13.5	7.0	10+2,-1	0.6

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## Vertical kink short lead (lead style: N / bulk)

			Table-2		Dime	nsion	(Unit : r	nm)
Your part No.	TDK part No.	T.C.	Cap. (pF)	C-Tol. (%)	D Max.			φd
	CD45SL2GA100JYNKA	SL	10	± 5	7.0	7.0	10+2,-1	0.6
	CD45SL2GA150JYNKA	SL	15	± 5	7.0	7.0	10+2,-1	0.6
	CD45SL2GA220JYNKA	SL	22	± 5	7.0	7.0	10+2,-1	0.6
	CD45SL2GA330JYNKA	SL	33	± 5	7.0	7.0	10+2,-1	0.6
	CD45SL2GA470JYNKA	SL	47	± 5	8.0	7.0	10+2,-1	0.6
	CD45SL2GA680JYNKA	SL	68	± 5	9.0	7.0	10+2,-1	0.6
	CD70-B2GA101KYNKA	В	100	± 10	7.0	7.0	10+2,-1	0.6
	CD70-B2GA151KYNKA	В	150	± 10	7.0	7.0	10+2,-1	0.6
	CD70-B2GA221KYNKA	В	220	± 10	7.0	7.0	10+2,-1	0.6
	CD75-B2GA331KYNKA	В	330	± 10	7.5	7.0	10+2,-1	0.6
	CD85-B2GA471KYNKA	В	470	± 10	9.0	7.0	10+2,-1	0.6
	CD65ZU2GA681MYNKA	Z5U	680	± 20	7.0	7.0	10+2,-1	0.6
	CD70ZU2GA102MYNKA	Z5U	1000	± 20	7.0	7.0	10+2,-1	0.6
	CD80ZU2GA152MYNKA	Z5U	1500	± 20	8.0	7.0	10+2,-1	0.6
	CD90ZU2GA222MYNKA	Z5U	2200	± 20	9.5	7.0	10+2,-1	0.6
	CD11ZU2GA332MYNKA	Z5U	3300	± 20	12.0	7.0	10+2,-1	0.6
	CD12ZU2GA472MYNKA	Z5U	4700	± 20	13.5	7.0	10+2,-1	0.6

Table-2



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#### Flow soldering recommended condition

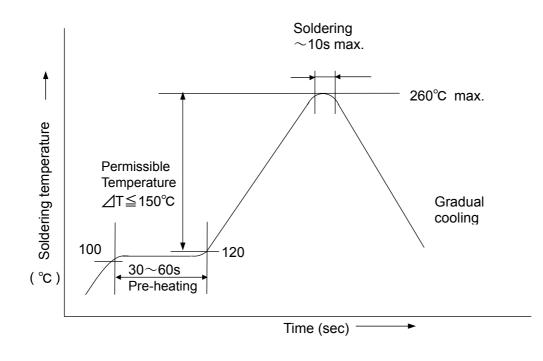


Fig-1

