

Rev. V2

#### **Features**

- Suitable for High Power Military and Civilian Radio Applications
- Power Handling: 100 W @ 85°C
  Insertion Loss: 0.35 dB @ 2 GHz
- Isolation: 51 dB @ 2 GHz
- Lead-Free 5 mm HQFN-12LD Package
- RoHS\* Compliant and 260°C Reflow Compatible

## **Description**

The MASW-011055 is a high power PIN diode SP2T switch in a common anode configuration, operating from 30 MHz to 3 GHz. It features low insertion loss and excellent linearity. This device is capable of handling 100 Watts CW incident power at a base plate temperature of 85°C.

This high power switch is ideal for use on land mobile radio and MIL-COM applications that require higher CW and pulsed power operation.

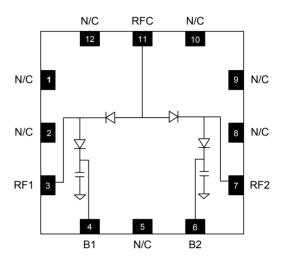
The MASW-011055 is manufactured using MACOM's hybrid manufacturing process featuring high voltage PIN diodes and passive devices integrated in a 5 mm HQFN 12-lead plastic package.

## Ordering Information<sup>1</sup>

Part Number	Package
MASW-011055-TR0500	500 piece reel
MASW-011055-SMB	Sample Board

1. Reference Application Note M513 for reel size information.

#### **Functional Schematic**



### **Pin Configuration**

Pin	Function	Pin	Function	
1	No Connection	7	RF2 / V2 Bias	
2	No Connection	8	No Connection	
3	RF1 / V1 Bias	9	No Connection	
4	B1 Bias	10	No Connection	
5	No Connection	11	RFC / V3 Bias	
6	B2 Bias	12	No Connection	
		Paddle <sup>2</sup>	Ground	

2. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

<sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

# MASW-011055



# Switch, SP2T 100 Watt Reflective 0.03 - 3.0 GHz

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## Electrical Specifications: $T_A = 25$ °C, $Bias^3 = +5 / -5 V$ , 50 mA / 100 mA

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss P <sub>IN</sub> = 0 dBm	0.5 GHz 1 GHz 2 GHz	dB	_	0.10 0.20 0.35	0.50 —
Isolation P <sub>IN</sub> = 0 dBm	0.5 GHz 1 GHz 2 GHz	dB	 48 	52 54 51	_
Input Return Loss	P <sub>IN</sub> = 0 dBm	dB	_	>15	_
CW Input Power	25°C base plate, 2 GHz	dBm W	_	52 158	_
CW Input Power	85°C base plate, 2 GHz	dBm W	_	50 100	_
P0.1dB	25°C base plate, 2 GHz	dBm	_	>52	_
Input IP3	F1 = 2000 MHz, F2 = 2010 MHz P <sub>IN</sub> = 40 dBm/Tone, 28 V	dBm	_	66	_
RF Switching Speed	(10-90% RF Voltage) 1 MHz Rep Rate in Modulating Mode	ns	_	500	_

<sup>3.</sup> See Bias table.

## Bias (+5 V / -5 V)

RF State	V1 Bias (V)	V2 Bias (V)	V3 Bias (V)	B1 Bias (V)	B2 Bias (V)
RFC – RF1 Low Loss RFC – RF2 Isolation	-5 V @ 100 mA	+5 V @ 50 mA	0 V	0 V	0 V
RFC – RF2 Low Loss RFC – RF1 Isolation	+5 V @ 50 mA	-5 V @ 100 mA	0 V	0 V	0 V

<sup>4.</sup> DC reverse bias of a PIN Diode operating at a high power is dependent on RF frequency, incident power, and VSWR. See Minimum Reverse DC Voltage table for high power operation.



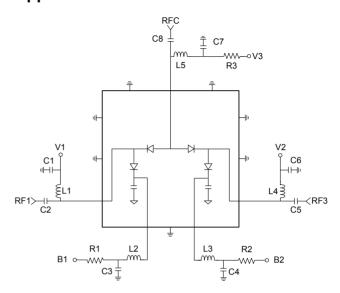
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## Minimum Reverse DC Voltage<sup>5</sup>

Frequency (MHz)	Minimum Reverse DC Voltage		
30	-120 V		
100	-119 V		
200	-114 V		
300	-106 V		
500	-90 V		
1000	-59 V		
1500	-43 V		
2000	-33 V		

<sup>5.</sup> Required to maintain low loss under 100 W of incident power with 1.5:1 VSWR.

## **Application Schematic**



## **Absolute Maximum Ratings**<sup>6,7</sup>

Parameter	Absolute Maximum	
Forward Current	200 mA	
Reverse DC Voltage	-150 V	
Operating Temperature	-40°C to +85°C	
Storage Temperature	-55°C to +150°C	
Junction Temperature	+175°C	

Exceeding any one or combination of these limits may cause permanent damage to this device.

## **Off-Chip Component Values**

0	Operating	perating Frequency		
Component	0.03 - 1.0 GHz	0.5 - 3.0 GHz	Size	
C1, C3, C4, C6, C7, C8	0.1 μF	270 pF	0603	
C2, C5, C8	0.1 μF	27 pF	0603	
L1 - L5	3.3 µH	82 nH	0603	
R1 - R2 <sup>8</sup>	82 Ω	82 Ω	1210	
R3 <sup>8</sup>	39 Ω	39 Ω	1210	

<sup>8.</sup> Resistance values are used for small signal testing under +5 V / -5 V bias conditions.

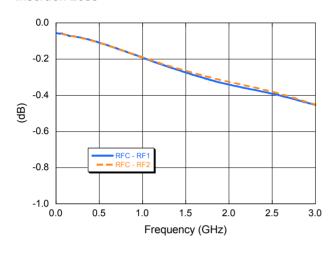
MACOM does not recommend sustained operation near these survivability limits.



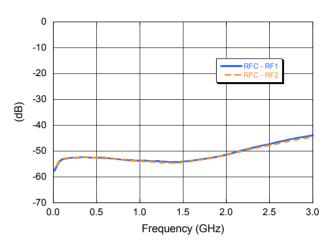
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## Typical Performance Curves @ +25°C (using external bias tees):

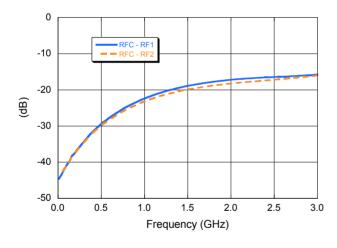
#### Insertion Loss



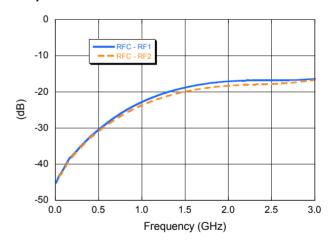
#### Isolation



#### Input Return Loss



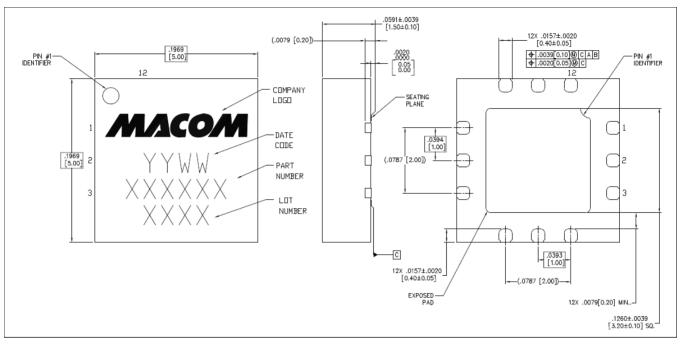
#### **Output Return Loss**





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### Lead Free 5 mm 12-Lead HQFN †



<sup>&</sup>lt;sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 1 requirements. Plating is NiPdAuAq.

#### **Handling Procedures**

Please observe the following precautions to avoid damage:

## **Static Sensitivity**

Silicon Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these Class 1B HBM devices.

## MASW-011055



Switch, SP2T 100 Watt Reflective 0.03 - 3.0 GHz

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